AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listing of claims in the application.

LISTING OF CLAIMS:

1. (Currently Amended) A computer system including a first computer node and a second computer node connected to said first computer node, comprising:

a first storage area for storing data records;

a first processor provided with said first computer node for storing the data records to said first storage area asynchronously with said second computer node with a free time interval;

a transmitter provided with said first computer node for sending the data records stored in said first storage area;

a second storage area for storing the data records copied from said first storage area;

a receiver provided with said second computer node and connected to said transmitter via a network for requesting said transmitter to send a record group of the data records stored in said first storage area via said network and designated by a request command sent by said receiver, receiving the record group of the data records from said transmitter via said network and storing the record group of the data records to said second storage area; and

a second processor for designating ~~a~~ the record group, ~~which includes at least a part of the data records,~~ to be read from said first storage area by using address information of said first storage area in a free time interval asynchronously with storing executed by said first processor and for letting said receiver send ~~a~~ the request command to said transmitter,

wherein said transmitter reads the record group designated by ~~said~~ the request command sent from said receiver and sends the record group to said receiver in response to the request command via said network.

2. (Original) A computer system as claimed in claim 1, wherein said first storage area is allocated within said first computer node.

3. (Original) A computer system as claimed in claim 1, wherein said second storage area is allocated within said second computer node.

4. (Previously Presented) A computer system as claimed in claim 1, wherein said first storage area is allocated within an external storage device connecting with said first computer node and said second computer node.

5. (Currently Amended) A computer system as claimed in claim 1, wherein said ~~second~~ computer node is provided with a timer for starting said second processor with a constant time interval to ~~read the data records~~ indicate said receiver to send the request command to said transmitter via said network~~to said second storage area from said first storage area~~.

6. (Currently Amended) A computer system as claimed in claim 1, wherein said first processor stores each record group of ~~said~~ the data records to said first storage area ~~by giving~~ with an identifier number indicating ~~the~~ a sequence of storing of the each record group of ~~said~~ the data records, ~~said first storage area includes a plurality of entries to store the set of said identifier number and the data records to read the data records from said entry in the inverse direction to the direction to write the data records to said entry with said first processor, and~~ said transmitter sends the record group to said receiver in reverse order of the sequence of storing, and said second processor refers to the record group of the data records in said first

storage area copied to said second storage area based on the reverse order in order to determine whether the relevant data records are correct or not depending on said identifier number.

7. (Currently Amended) A computer system as claimed in claim 6, wherein said first processor writes the identifier number of the relevant record group of the data records after having written ~~said~~ the data records and said second processor determines that the relevant record group of the data records are correct when ~~said~~ the identifier number of the relevant record group of the data records read to said second storage area has continuity ~~but the relevant data records are incorrect when said identifier number does not have continuity~~.

8. (Currently Amended) A computer system as claimed in claim 1, wherein said first processor further includes an error checking code generator for generating an error checking code for ~~said~~ the record group of the data records to write ~~said~~ the record group of the data records and ~~said~~ the error checking code to said first storage area and said second processor checks an error, ~~with said~~ by use of the error checking code, of the record group of the data records read to said second storage area ~~and determines that the relevant data records are correct when no error is checked or incorrect when an error is checked~~.

9. (Previously Presented) A computer system as claimed in claim 8, wherein said first storage area includes a plurality of entries for storing a set of said error checking code and the data records to read the data records in the inverse direction to the direction to write the data records to said entry with said first processor.

Claims 10-15 (Canceled)

16. (New) A computer system comprising:

a first computer node including a first processor, a first memory coupled to said first processor, and a transmitter coupled to said first processor and said first memory;

a second computer node including a second processor, a second memory coupled to said second processor, and a receiver coupled to said second processor and said second memory; and

a network being coupled to said transmitter and said receiver,

wherein said first processor stores a plurality of data records into said first memory based on a first timing,

wherein said second processor indicates said receiver to send a read request, which includes information indicating a part of the plurality of data records stored in said first memory, to said transmitter via said network based on a second timing which is independent of the first timing of storing the plurality of data records into said first memory, and said second processor makes the information indicating a part of the plurality of the data records by using address information of said first memory,

wherein said receiver sends the read request to said transmitter via said network in response to the indication of said second processor and said transmitter reads the part of the plurality of data records from said first memory and sends the part of the plurality of data records to said receiver via said network in response to the read request, and

wherein said receiver stores the part of the plurality of data records received from said transmitter into said second memory.

17. (New) A computer system as claimed in claim 16, wherein said second computer node includes a timer and the second timing is created by said second processor based on said timer.

18. (New) A computer system as claimed in claim 17, wherein said first processor stores the plurality of data records with a sequence number, wherein said second processor makes the information indicating a part of the plurality of data records stored in said first memory based on the sequence number, wherein said transmitter sends the part of the plurality of data records in reverse order of the sequence number, and wherein said receiver stores the part of the plurality of data records into said second memory in reverse order of the sequence number.